

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	3	timer adj circuit and ramp adj timer and counter and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:21
S2	177	timer adj circuit with counter with (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:19
S3	0	timer adj circuit with ramp with counter with (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:18
S4	9	timer with ramp with counter with (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:16
S5	1	timer adj circuit with ramp adj timer and counter	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:18
S6	0	timer adj circuit same end adj2 ramp same counter same (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:20
S7	1	timer adj circuit same ramp same counter same (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:20
S8	47	tim\$3 adj circuit and ramp adj tim\$3 and counter and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:02
S9	989	tim\$3 adj circuit and ramp\$3 and counter and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:12
S10	2	tim\$3 adj circuit with ramp\$3 with counter with (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:22
S11	140	tim\$3 adj circuit.clm. and ramp\$3 and counter and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:12
S12	15	tim\$3 adj circuit.ti. and ramp\$3 and counter and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:12

S13	12	S9 and S11 and S12	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 15:58
S14	9	("5124597").URPN.	USPAT	OR	ON	2005/03/08 15:41
S15	6	("3576496" "4604536" "4611136" "4843255" "4857760" "4954784").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/03/08 15:46
S16	3	S12 not S13	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:02
S17	46	S8 not S12	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:02
S18	0	tim\$3 adj circuit same ramp adj tim\$3 same counter same (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:03
S19	6	tim\$3 adj circuit same ramp with generat\$3 same counter same (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:03
S20	2	("5233232").PN.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/08 16:09
S21	1078	tim\$3 adj circuit and ramp\$3 and count\$3 and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:15
S22	148	tim\$3 adj circuit.clm. and ramp\$3 and count\$3 and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:12
S23	15	tim\$3 adj circuit.ti. and ramp\$3 and count\$3 and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:13
S24	136	S21 and S22 not S13	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:13
S25	128	S9 and S11 not S12	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:14

S26	128	S24 and S25	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:14
S27	150	tim\$3 adj circuit same ramp\$3 and count\$3 and (latch or flip adj flop)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:15
S28	34	S26 and S27	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/08 16:15